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HW

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/712,474	11/14/2000	Anil Raj Duggal	RD-28,259	9029

6147 7590 11/05/2003

GENERAL ELECTRIC COMPANY  
GLOBAL RESEARCH CENTER  
PATENT DOCKET RM. 4A59  
PO BOX 8, BLDG. K-1 ROSS  
NISKAYUNA, NY 12309

EXAMINER
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VU, JIMMY T

ART UNIT	PAPER NUMBER
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2821

DATE MAILED: 11/05/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application N .

09/712,474

Applicant(s)

DUGGAL ET AL.

Examiner

Jimmy T Vu

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*BW*

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 06 August 2003.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-29, 46-49 and 51 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 4-29 is/are allowed.
- 6) ☒ Claim(s) 1-3, 46-49 and 51 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

**Priority under 35 U.S.C. §§ 119 and 120**

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)                  | 4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s). _____  |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)         | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____ | 6) <input type="checkbox"/> Other: _____                                    |

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## **DETAILED ACTION**

### ***Response to Arguments***

1. Applicant's arguments with respect to claims 1-3 and 46-51 have been considered but are moot in view of the new ground(s) of rejection.

Despite applicant's disagreement, the examiner decides to provide new rejection as below in view of the plurality of organic light-emitting diodes modules electrically connected in series.

### ***Claim Rejections - 35 USC § 102***

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1-3 and 46-51 are rejected under 35 U.S.C. 102(e) as being anticipated by Ovshinsky (U.S. Patent number 3,763,468).

Regarding claim 1, Ovshinsky discloses a light emitting device comprising:

a plurality of organic light emitting diode (OLED) modules (14) (Figs. 1-4, 8, 9 and 13) electrically connected in series, each of said OLED module comprises an organic layer (Figs. 1, 14 and 15), which emits light when activated; and

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an alternating current (AC) power source (32) (Figs. 8 and 13) electrically connected to and providing an AC voltage directly to the plurality of OLED modules, said voltage and said current inherently having a waveform characteristic (Figs. 1-15, col. 3, lines 10-50).

Regarding claim 2, Ovshinsky discloses a light emitting device comprising:

a substrate (90) (Fig. 15); and

a plurality of organic light emitting diode (OLED) series groups (Figs. 1 and 13) provided on the substrate (90), each OLED series group comprising a plurality of OLED modules (14), the OLED modules of each OLED series group electrically connected in series, wherein each OLED modules comprises an organic layer that emits light when activated, and the OLED modules emit light upon application of an AC voltage supplied directly thereto, and the AC voltage has a waveform characteristic (Figs. 1-15, col. 3, lines 10-50).

Regarding claim 3, Ovshinsky discloses the light emitting device further comprising:

at least one first conducting line (20-25) provided on the substrate, the at least one first conducting line electrically connected to a first end of each OLED series group; and

a second conducting line (26-31) provided on the substrate, the second conducting line electrically connected to a second end of each OLED series group opposite the first end (Fig. 1, col. 3, lines 20-25).

Regarding claims 46 and 47, Ovshinsky discloses a display comprising a plurality of organic light emitting diode (OLED) modules arranged to spell out at least one letter or depict an

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image, wherein each OLED module has a shape of a letter or image, and the plurality of the OLED modules are connected electrically in series (Fig. 14, col. 3, lines 10-50).

Regarding claim 48, Ovshinsky discloses a display comprising a plurality of organic light emitting diode (OLED) modules (14) arranged to spell out at least one letter or depict an image (Fig. 14, col. 3, lines 10-50),

wherein the plurality of OLED modules is grouped into a plurality of series groups, and the OLED modules of each series group are electrically connected in series (Fig. 1-15, col. 3, lines 10-50).

Regarding claim 49, Ovshinsky discloses the display wherein each OLED modules has the shape of a letter or image (Fig. 14).

Regarding claim 51, Ovshinsky discloses a method of making a display comprising: providing a substrate (20); arranging a plurality of organic light emitting diode (OLED) modules (14) to spell out at least one letter or depict an image (Figs. 14 and 15); and providing electrical connections between the plurality of OLED modules electrically to connect the plurality of OLED modules in series (Figs. 1-15, col. 3, lines 10-50).

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*Allowable Subject Matter*

4. Claims 4-28 are allowed.

None of the prior art teaches the converting circuit that converts an applied AC voltage with the sinusoidal waveform to the converted voltage waveform and applies the converted voltage waveform to the at least one first and the second conducting lines.

*Conclusion*

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jimmy Vu whose telephone number is (703) 306-5451. The examiner can normally be reached on Monday to Friday from 9:00am to 6:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Don Wong, can be reached on (703) 308-4856. The fax phone number for the organization where this application or proceeding is assigned is (703) 308-7722 or (703) 308-7724.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center receptionist whose telephone number is (703) 308-0956.

Jimmy Vu

October 28, 2003

